

WHAT IS CLAIMED IS:

1. A plasma display panel comprising a rear substrate and a front substrate, wherein the rear substrate is spaced a predetermined distance apart from the front substrate and wherein the rear substrate faces the front substrate, and a plurality of discharge cells are formed between the front substrate and the rear substrate, the plasma display panel comprising:

a heating portion disposed at a rear of the rear substrate to heat the rear substrate and the front substrate.

2. The plasma display panel of claim 1, wherein the heating portion comprises a heat generating body and a controlling portion for controlling the heat generating body to generate heat only at a predetermined temperature or less.

3. The plasma display panel of claim 2, wherein the controlling portion comprises a circuit portion for allowing a current to flow into the heat generating body according to a sensed temperature.

4. The plasma display panel of claim 3, wherein the circuit portion comprises a thermistor having a resistance, wherein the resistance varies according to the sensed temperature, and a field effect transistor for

allowing the current to flow into the heat generating body according to a level of the resistance of the thermistor.

5. The plasma display panel of claim 2, wherein the heat generating body comprises a heat generating coil.

6. The plasma display panel of claim 2, wherein the predetermined temperature is approximately 0 °.

7. A plasma display panel comprising:

a rear substrate and a front substrate, wherein the rear substrate is spaced a predetermined distance apart from the front substrate and wherein the rear substrate faces the front substrate, and a plurality of discharge cells are formed between the front substrate and the rear substrate;

a plurality of first electrodes formed on an inner surface of the rear substrate;

a first dielectric layer formed on the inner surface of the rear substrate, to cover the plurality of the first electrodes;

a plurality of partitions formed on a surface of the first electric layer to define the discharge cells;

a phosphor layer formed on sidewalls of the partitions and on a surface of the first dielectric layer;

a plurality of second electrodes formed on an inner wall of the front substrate, corresponding to the plurality of the first electrodes;

a second dielectric layer formed on the inner wall of the front substrate to cover the plurality of the second electrodes;

a protective layer formed on a surface of the second dielectric layer;
and

a heating portion disposed at a rear of the rear substrate to heat the rear substrate and the front substrate.

8. The plasma display panel of claim 7, wherein the heating portion comprises a heat generating body and a controlling portion for controlling the heat generating body to heat only at a predetermined temperature or less.

9. The plasma display panel of claim 8, wherein the controlling portion comprises a circuit portion for allowing a current to flow into the heat generating body according to a sensed temperature.

10. The plasma display panel of claim 9, wherein the circuit portion comprises a thermistor having a resistance, wherein the resistance varies according to the sensed temperature, and a field effect transistor, wherein the field effect transistor allows the current to flow into the heat generating body according to a level of the resistance of the thermistor.

11. The plasma display panel of claim 7, wherein the protective layer is formed of MgO.

12. The plasma display panel of claim 8, wherein the heat generating body comprises a heat generating coil.

13. The plasma display panel of claim 8, wherein the predetermined temperature is approximately 0°C.